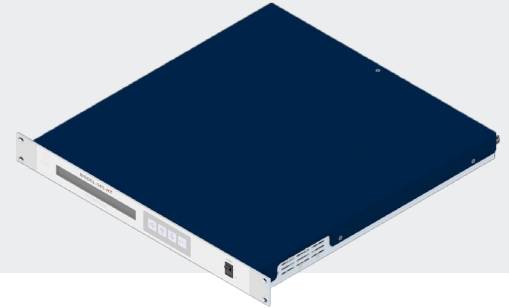


TREK 645-HT

Software-driven electrostatic chuck controller that offers an array of beneficial features while providing more DC current for applications with higher process temperatures.



The Trek® 645-HT software-driven electrostatic chuck controller provides a significant increase in efficiency and throughput compared to electrostatic chuck power supplies. The Trek 645-HT series virtually eliminates sticky wafer and wafer popping issues, thus ensuring better control over particle contamination. Given the versatility and performance of the Trek 645-HT series, it can be used in a variety of unique high-temperature applications, thus eliminating the need to specify a new supply for each tool/process in a facility.

PRODUCT HIGHLIGHTS

- Supports both Coulombic and Johnsen-Rahbek ESC technologies
- Configurable for custom clamp/declamp sequences and wave shapes
- Electrostatic chuck profiles can be uploaded to the unit and stored internally via the user-friendly software interface
- Parameters can be controlled via the front panel, digital communications interfaces, or analog I/O pins.
- Increased throughput and elimination of sticky/popping wafers
- Lockable front panel control interface
- Control parameters such as over-current, wafer-present and wafer-clamped thresholds, clamp voltage, and offset voltage
- Wafer detection includes no wafer, wafer present, or wafer clamped status
- Adjustable in-process amplitude/offset and output-control versatility
- NIST-traceable Certificate of Calibration provided with each unit

AT A GLANCE

Output Voltage Range

0 to ± 1 kV

Output Current Range

0 to ± 20 mA

TYPICAL APPLICATIONS

- Electrostatic-driven handling of materials
- Semiconductor wafer processing
- Non-mechanical transfer of flat panels or other processing materials sensitive to mechanical clipping

TREK 645-HT ELECTROSTATIC CHUCK CONTROLLER

TECHNICAL DATA

Outputs		
Simultaneous High Voltage Outputs	Two simultaneous high-voltage outputs (Output Phase A and Output Phase B) of equal magnitude and opposite in polarity relative to an offset voltage	
Output Phasing	Output Voltage A (Reference Phase)	0 to ± 1 kV
	Output Voltage B	0 to ± 1 kV (Phase B = $[-1] \times$ Phase A)
	Offset Voltage	An offset voltage of up to ± 1 kV may be simultaneously added to the output of each phase. When an offset voltage is added, the polarity and magnitude of offset voltage appears simultaneously on both outputs A and B regardless of the magnitude and polarity of the A and B voltages themselves. The sum of the offset voltage and the output voltage of each phase cannot exceed ± 1 kV
Output Waveshape	Each DC output voltage (Phase A and Phase B) is ramped up and down with symmetrical rise and fall times, or can be programmed with the user's custom clamping and declamping waveforms	
Output Voltage Range	0 to ± 1 kVDC, maximum	
Output Current	0 to ± 20 mA	

Input	
Setting the High Voltage Amplitude	HV magnitude can be controlled either externally or internally to the unit
Setting the Offset Voltage	Offset voltage may be controlled externally or internally to the unit

Output Voltage Monitor (Back Panel Connector)	
Scale Factor	1 V/150 V
Phase B DC	Accuracy better than 2% of full scale
Offset Voltage	Less than 10 mV
Output Noise	Less than 50 mV rms ¹

Steady State Voltage Leakage Current Monitor	
Scale Factor	1 V / 5 mA
DC Accuracy	± 500 μ A
Output Noise	Less than 50 mV rms ¹

Mechanical Specifications	
Dimensions (H x W x D)	43.7 x 421.6 x 457.3 mm (1.72 x 16.6 x 18 in) 1U rack enclosure
Panel Width	482.6 mm (19 in)
Weight	3.86 kg (8.5 lb)
Connectors	15-pin "D" ITT Canon used by remote device to control/monitor the unit, 9-pin "D" ITT Canon RS232, 3-Pin FCT "D" High-Voltage, standard type-A USB, Ethernet (optional) and Front Panel
Power ON/OFF	Two-position rocker switch

Electrical Specifications	
DC Input Receptacle	2.0 mm locking DC jack; center contact is positive and shell is negative (receptacle mates with Switchcraft S761K plug)
Ground Receptacle	Ground stud
Power Requirements	24 VDC, 1.7 A

¹ Measured using the true rms feature of the HP Model 34401A digital multimeter

TECHNICAL DATA

Environmental Specifications

Temperature	0 to 35°C (32 to 104°F)
Relative Humidity	To 85%, noncondensing
Altitude	To 2000 m (6561.68 ft)

Features

Interlock	Connections are provided to support an interlock safety configuration. In the event that the interlock is open, the high-voltage generation circuits are shut down
Digital Display	40 x 2 LCD character display shows various system functions such as Set Voltage, Output Voltage and Capacitance Monitor
Clamped Wafer Detection Feature (Thresholds are set by the program)	To indicate wafer clamping events, the capacitive currents generated by a low voltage sine wave, super-imposed on the Phase A and Phase B outputs, are monitored but can be disabled through the program. The superimposed waveform is used to indicate a no wafer, wafer present, or wafer clamped status
Capacitive Load Select	Clamped capacitance status range can be selected by the program for 0 to 10, 20 or 30nF (phase to phase) depending on the system and electrostatic clamp physical configurations

REFERENCE NUMBERS

Included Accessories

PN	Description
970-220624-0000	Operator's Manual, Software
TK-BA103	USB Cable
TK-B8084R, TK-B8085R, TK-B8088R, TK-B8089R	HV Connectors
Varies	Line Cord, Spare Fuses (selected per geographic region)

Optional Accessories

PN	Description
TK-1K045E	90 to 264 VAC to 24 VDC Power Adapter



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Our products enable customer innovation in complex applications for a wide range of industries including semiconductor equipment, industrial, manufacturing, telecommunications, data center computing, and medical. With deep applications know-how and responsive service and support across the globe, we build collaborative partnerships to meet rapid technological developments, propel growth for our customers, and innovate the future of power.

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